

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Jeffrey Lukanc

Assignee: Integrated Device Technology, Inc.

Title: TEST BUS ARCHITECTURE FOR EMBEDDED RAM AND METHOD
OF OPERATING SAME

Serial No: 09/887,913

Group No.: 2133

Filed: 6/20/2001

Examiner: Guy Lamarre

Attorney Docket No: IDT-1616

March 13, 2006

Mail Stop Amendment
Commissioner of Patents and Trademarks
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO FOURTH OFFICE ACTION

Sir:

Applicant submits the following remarks in response to the Office Action dated September 12, 2005. A Request for Extension of Time, extending the period for response to March 12, 2006, accompanies the present response.